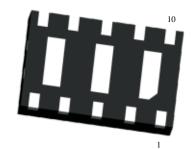


# Low Capacitance ESD TVS Array in DFN3020

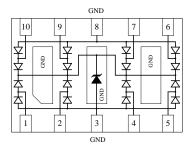
#### **Features**

- 1000Watts peak pulse power (tp = 8/20µs)
- DFN3020 package
- Unidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (1.7pF typical I/O to I/O)
- ESD Protection for high-speed data lines to: IEC 61000-4-2 ±30KV contact ±30KV air IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 40A (8/20µs)



#### **Mechanical Data**

- Case: DFN3020 (plastic package). Lead free; RoHS compliant
- Molding Compound Flammability Rating: UL 94 V-0
- **Terminals:** High temperature soldering guaranteed: 260 °C/10 sec. at terminals



### **Applications**

- 10/100/1000M Ethernet Ports
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- Cellular Phones
- Switching Systems
- Audio/Video Inputs

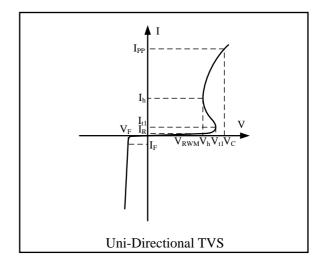
#### **Absolute Maximum Ratings**

Ratings at 25 °C, ambient temperature unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Pulse Power (T <sub>P</sub> =8/20μS)	P <sub>PP</sub>	1000	W
ESD contact/air discharge (IEC-61000-4-2)	V <sub>ESD</sub>	30/30	kV
Peak Pulse Current ( t₂ = 8/20μS )	I <sub>PP</sub>	40	А
Junction Temperature	TJ	-55 to +125	°C
Storage temperature	T <sub>STG</sub>	-55 to +150	$^{\circ}$



Symbol	Parameter	
$V_{RWM}$	Nominal Reverse Working Voltage	
$I_R$	I <sub>R</sub> Reverse Leakage Current @ V <sub>RWM</sub>	
$V_{t1}$	V <sub>t1</sub> Trigger Voltage	
$I_{t1}$	Trigger Current @ V <sub>t1</sub>	
$V_h$	V <sub>h</sub> Holding Voltage	
$I_h$	I <sub>h</sub> Holding Current @ V <sub>h</sub>	
$V_{\rm C}$	V <sub>C</sub> Clamping Voltage @ I <sub>PP</sub>	
$I_{PP}$	I <sub>PP</sub> Maximum Peak Pulse Current	
$C_{ESD}$	C <sub>ESD</sub> Parasitic Capacitance	



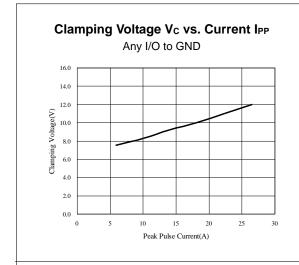
# **Electrical Characteristics**

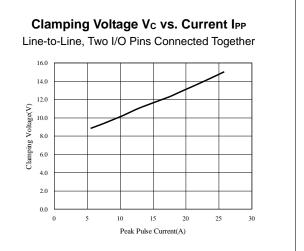
(T<sub>A</sub> = 25 °C unless otherwise specified)

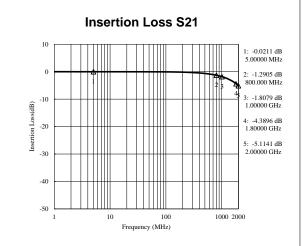
Symbol	Test Condition	Minimum	Typical	Maximum	Units
$V_{ m RWM}$				3.3	V
$I_R$	$V_{RWM} = 3.3V, T = 25^{\circ}C$		0.1	1.0	μΑ
$V_{t1}$	$I_{t1}=1\mu A$	3.8	4.5	5.5	V
$V_{\rm h}$	$I_h = 1mA$	3.5		5.5	V
V <sub>C</sub>	Any I/O to Ground $I_{PP} = 1A$ , $t_p = 8/20\mu s$			5.5	V
V <sub>C</sub>	Any I/O to Ground $I_{PP} = 10A$ , $t_p = 8/20\mu s$			10.5	V
$V_{\rm C}$	Any I/O to Ground $I_{PP} = 25A$ , $t_p = 8/20\mu s$			18.0	V
$V_{\rm C}$	Line-to-Line / Line-to-GND, two I/O Pins connected together on each line $I_{PP}=40A,t_p=8/20\mu s$			25.0	V
$C_{ESD}$	Between I/O Pins and Ground $V_R = 0V$ , $f = 1MHz$		3.8	5.0	pF
$C_{ESD}$	Between I/O Pins $V_R = 0V$ , $f = 1MHz$		1.7	2.5	pF

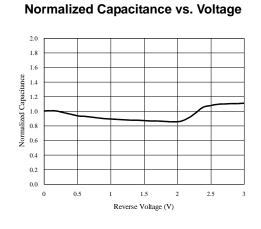


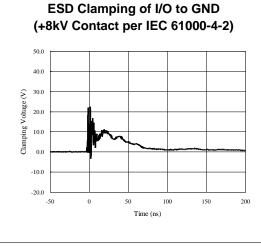
# Typical Characteristics ( $T_{amb} = 25 \, ^{\circ}\text{C}$ unless otherwise specified)

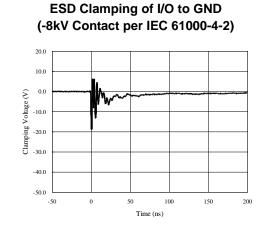




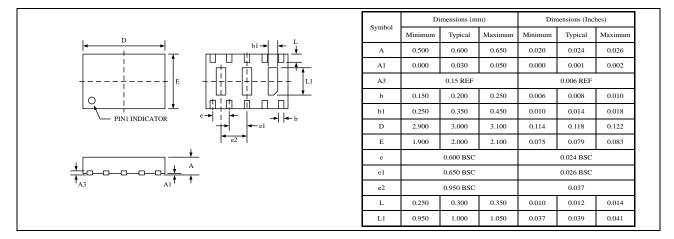




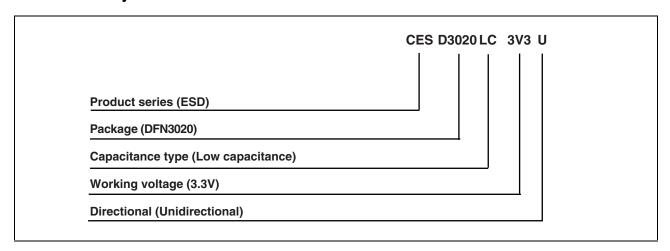




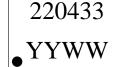
# **Package Dimensions**



### Part number system



# Marking



#### Note:

- (1) "220433" is the part number, fixed.
- (2) "YYWW" is date code. "YY" is year (2013 is "13"); while "WW" is the assembly week in a year.

### **Ordering inormation**

	Order code	Package	Packaging option	Base quantity	Packaging specification
I	CESD3020LC3V3U	DFN3020	Tape and reel	3000pcs / reel	EIA STD RS-481

### **Reision history**

Date	Revision	Changes
23-May-2012	1.0	Initial release



# CESD3020LC3V3U

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